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## N THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

PARK, Ji Soo et al.

Conf.:

5713

Appl. No.:

09/998,131

Group:

2825

Filed:

December 3, 2001

Examiner:

B. KESHAVAN

For:

SEMICONDUCTOR DEVICE HAVING A GATE AND

FABRICATION METHOD THEREFOR

## PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Monday, August 18, 2003

Sir:

In response to the Examiner's final Office Action dated December 16, 2003, further to the filing of a Request for Reconsideration on April 16, 2003, in response to the Advisory Action dated May 1, 2003, further to the filing of a Notice of Appeal on May 16, 2003, and in view of the Request for Continued Examination (RCE) being filed concurrently herewith, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This Reply includes:

Amended Claim Set; and

Remarks.

AUG 20 2003
TECHNOLOGY CENTER 2800